



Fig.1 (PRIOR ART)

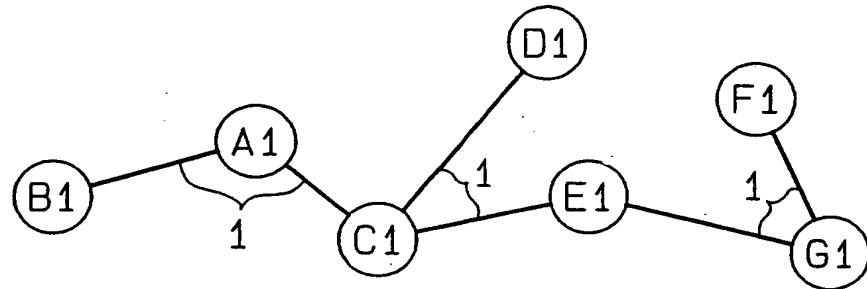
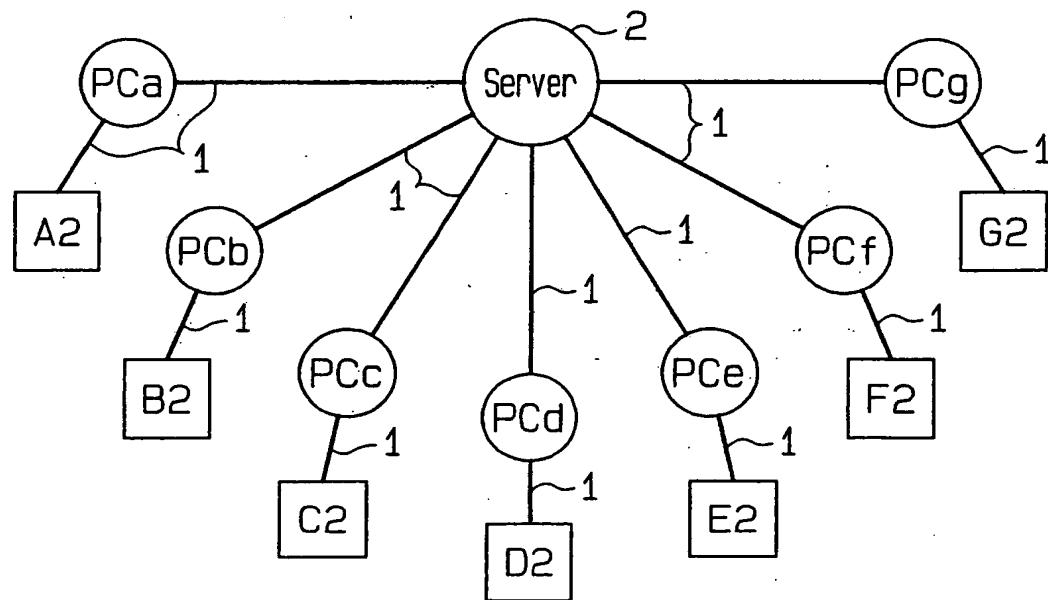


Fig. 2 (PRIOR ART)

Bus Cables ab ef ab ef ab ef

Fig. 3 (PRIOR ART)



TITLE: PACKET TRANSFER CONTROL
CIRCUIT
INVENTORS: Kenji Oi et al.
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REPLACEMENT SHEET



Fig. 4
(PRIOR ART)

